

APPLICATION DATA SHEET

APPLICATION INFORMATION

Filing Date:: 03-02-04
Application Type:: Regular
Subject Matter:: Utility
Title:: Bit-Edge Zero Forcing Equalizer
Attorney Docket Number:: X-1549 US
Request for Early Pub?:: No
Request for Non-Pub?:: Yes
Total Drawing Sheets:: 10
Small Entity?:: No
Petition included?:: No

APPLICANT INFORMATION

Applicant Authority Type:: Inventor
Primary Citizenship Ctry:: US
Status:: Full Capacity
Given Name:: Brian
Middle Name:: T.
Family Name:: Brunn
Street:: 2803 Lenwood Court
City:: Austin
State or Province:: Texas
Postal or Zip Code:: 78738

Applicant Authority Type:: Inventor
Primary Citizenship Ctry:: US
Status:: Full Capacity
Given Name:: Stephen
Middle Name:: D.
Family Name:: Anderson
Street:: 15161 Williston Lane
City:: Minnetonka
State or Province:: Minnesota

Postal or Zip Code:: 55345

CORRESPONDENCE INFORMATION

Correspondence Customer Number:: 24309

REPRESENTATIVE INFORMATION

Representative Customer Number::	24309	
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ASSIGNEE INFORMATION

Assignee Name:: Xilinx, Inc.
Street:: 2100 Logic Drive
City:: San Jose
State or Province:: California
Postal or Zip Code:: 95124